# Week of 1/31/2014

Over the last week here are what the individual group members worked on:

Rob (technical lead): Created RTx Controller Design Document, reviewed and completed Axis Position Feedback section. Started design of Manual Remote connector board.

Robert: Research and decide on Ethernet switch (found an Ethernet switch that can support PoE within our budget, still researching Sightline power requirements to determine whether PoE is best option). Continued research on Ethernet PHY implementation.

Robin (project manager): wrote up Project description, adjusted Gantt chart to better fit our project timeline. Research on implementation of USB.

# Week of 1/24/2014

Over the last week here are what the individual group members worked on:

Rob: Trained Robert on Eagle schematic design and new part creation. Researched PSAS Ethernet node-to-node firmware code base. Modified ADC page of RTx Controller Board Schematic.

Robert: Assisted with project scheduling, verified microcontroller decision, researched Ethernet switches, researched Ethernet PHY layer design.

Robin: Gantt chart and worked on project layout and scheduling. Determined initial tasks each member will take and organized overall structure. Set up to present project proposal with PSAS, worked on requirements document.

# Week of 1/17/2014

At this time, our group is beginning research for the various aspects of the project, completing the project design specifications and proposal, as well as creating a task list so that we can move forward with the design project.

Over the last week here are what the individual group members worked on:

Rob: Developed the system level 0 block diagram and controller level 1 block diagram. Attended PSAS meeting and met Andrew Tuesday night. Helped to develop the initial task list.

Robin: created Project Design Specifications document. Attended PSAS meeting and met with Andrew to determine to discuss mechanical component of project. Began work on Gantt chart for scheduling. Worked on high level task list.

Robert: Researched design implementations for the ADC, USB, and Ethernet components of the controller. Was able to confirm the ADC setup that exists is correct. Presented this knowledge at group meeting.